

REMARKS

The Examiner's Office Action of November 22, 2002 has been received and its contents reviewed. Applicants would like to thank the Examiner for the consideration given to the above-identified application.

By the above actions, claims 1, 2, 6, 11, 14, 17, 18 and 22 have been amended and new claims 27-32 has been added. In view of these actions and the following remarks, reconsideration of this application is now requested.

Referring now to the detailed Office Action, claims 1, 2, 4-7, 9-18, 20-23, and 25-26 stand rejected under 35 U.S.C. §102(e) as anticipated by Sawada (U.S. Patent No. 6,078,317). Further, claims 4, 9, 20, and 24 stand rejected under 35 U.S.C. §103(a) as unpatentable over Sawada in view of Jong (U.S. Patent No. 6,008,801).

As amended, independent claims 1, 6, 11, 14, 17, and 22 have been amended so as to include a feature in which the control circuit feeds pulses directly to the display panel and the image signal processing circuit. This feature is supported at least in, e.g., the last paragraph of page 6, and in Fig. 1 of the present application.

Sawada, the primary reference, fails to teach, disclose, or suggest the amended feature because Sawada teaches the controller (17) controls the display panel via the scanning control circuit (22).

Consequently, since each and every feature of the present claims is not taught (and is not inherent) in the teachings of Sawada, as is required by MPEP Chapter 2131 in order to establish anticipation, the rejection of claims 1, 2, 4-7, 9-18, 20-23, and 25-26, under 35 U.S.C. §102(e), as anticipated by Sawada is improper.

As Sawada is deficient, as discussed above, the combination of Sawada and Jong in a §103(a) rejection would also be improper.

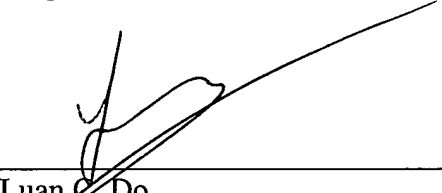
In view of the amendments and arguments set forth above, Applicants respectfully request reconsideration and withdrawal of all pending claim rejections.

While the present application is now believed to be in condition for allowance, should the Examiner find some issue to remain unresolved, or should any new issues arise, which could be

eliminated through discussions with applicants' representative, then the Examiner is invited to contact the undersigned by telephone in order that the further prosecution of this application can thereby be expedited.

Lastly, it is noted that a separate Extension of Time Petition accompanies this response along with a check in payment of the requisite extension of time fee. However, should that petition become separated from this Amendment, then this Amendment should be construed as containing such a petition. Likewise, any overage or shortage in the required payment should be applied to Deposit Account No. 19-2380 (740819-488).

Respectfully submitted,



Luan C. Do
Registration No. 38,434

NIXON PEABODY LLP
8180 Greensboro Drive, Suite 800
McLean, VA 22102
(703) 770-9300

MARKED UP VERSION

IN THE CLAIMS:

In the pending claims, please amend claims 1, 2, 6, 11, 14, 17, 18 and 22 as follows.

1.(Amended) A display device comprising:

a display panel comprising a pixel portion in which a plurality of thin film transistors are arranged in a matrix, a source driver circuit, and a gate driver circuit;

an image signal processing circuit for processing an image signal input from an external source; and

a control circuit [for controlling] which feeds pulses directly to said display panel and said image signal processing circuit,

wherein said image signal processing circuit corrects said image signal on a basis of a correction table and feeds said display panel with said corrected image signal.

2.(Amended) A display [panel] device according to claim 1, wherein said display panel is a liquid crystal display panel.

6.(Amended) A display device comprising:

a display panel comprising a pixel portion in which a plurality of thin film transistors are arranged in a matrix, a source driver circuit, and a gate driver circuit;

an image signal processing circuit for processing an image signal input from an external source; and

a control circuit [for controlling] which feeds pulses directly to said display panel and said image signal processing circuit,

wherein said image signal processing circuit performs gamma correction on said image signal on a basis of a correction table and feeds said display panel with said image signal on which gamma correction has been performed.

11.(Amended) A method for operating a display device comprising the steps of:
processing an image signal input from an external source by an image signal processing circuit;
feeding pulses directly to said image signal processing circuit and a display panel by a control circuit;
correcting said image signal based on a correction table; and
supplying a corrected image signal to said display panel through a correction circuit.

14.(Amended) A method for operating a display device comprising the steps of:
processing an image signal input from an external source by an image signal processing circuit;
feeding pulses directly to said image signal processing circuit and a display panel by a control circuit;
performing a gamma correction of said image signal based on a correction table; and
supplying a corrected image signal to said display panel through a correction circuit.

17.(Amended) A display device comprising:
a display panel comprising a pixel portion in which a plurality of thin film transistors are arranged in a matrix, a digital video signal dividing circuit, a source driver circuit, and a gate driver circuit;
an image signal processing circuit for processing an image signal input from an external source; and
a control circuit which feeds pulses directly to said display panel and said image signal processing circuit,
wherein said image signal processing circuit corrects said image signal on a basis of a correction table and feeds said display panel with said corrected image signal.

18.(Amended) A display device according to claim 17, wherein said display panel is a

liquid crystal display panel.

22.(Amended) A display device comprising:

a display panel comprising a pixel portion in which a plurality of thin film transistors are arranged in a matrix, a digital video signal dividing circuit, a source driver circuit, and a gate driver circuit;

an image signal processing circuit for processing an image signal input from an external source; and

a control circuit which feeds pulses directly to said display panel and said image signal processing circuit,

wherein said image signal processing circuit performs gamma correction on said image signal on a basis of a correction table and feeds said display panel with said image signal on which gamma correction has been performed.